

81754.0048

FORM PTO-1083

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:  
Toshihiko HIGUCHI  
Serial No: 09/754,818  
Filed: January 4, 2001  
For: SEMICONDUCTOR DEVICE HAVING A WIRING  
PATTERN AND METHOD FOR MANUFACTURING  
THE SAME

Art Unit: 2814  
Examiner: Phat X. Cao

I hereby certify that this correspondence  
is being transmitted via facsimile to (703)  
872-9319: Commissioner for Patents,  
Washington D.C. 20231, on  
March 14, 2003  
Date of Deposit  
Diane Zynn  
Name  
Diane Zynn 03/14/03  
Signature Date

Box AF  
Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

Transmitted herewith is an Amendment in the above-identified application.

☐ Small entity status has been claimed. See 37 CFR § 1.27.

☒ No additional fee is required.

The fee has been calculated as shown below:

	(Col. 1) CLAIMS REMAINING AFTER AMENDMENT	(Col. 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Col. 3) PRESENT EXTRA*	LG/SM \$ ENTITY FEE	ADD'L FEE DUE
TOTAL CLAIMS FEE	20	20	0	LG=\$18 SM=\$9	\$ 0
INDEPENDENT CLAIMS FEE	7	8	0	LG=\$84 SM=\$42	\$ 0
FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIMS				LARGE ENTITY FEE = \$280 SMALL ENTITY FEE = \$140	\$ 0
Independent Claims: 1, 7, 8, 9, 10, 11, 12				TOTAL	\$ 0

- \* If the entry in Col. 1 is less than the entry in Col. 2, write "0" in Col. 3.
- \*\* If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, write "20" in this space.
- \*\*\* If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, write "3" in this space. The "Highest Number Previously Paid For" (Total or Independent) is the highest number found from the equivalent box on Col. 1 of a prior amendment or the number of claims originally filed.

- ☐ A check in the amount of \$\_\_\_\_\_ to cover the additional claims fee is enclosed. A copy of this sheet is enclosed.
- ☐ A check in the amount of \$\_\_\_\_\_ to cover the extension of time fee is enclosed. A copy of this sheet is enclosed.
- ☒ The Commissioner is hereby authorized to charge any deficiencies of fees associated with this communication or credit any overpayment to Deposit Account No. 50-1314. A copy of this sheet is enclosed.
- ☒ Any filing fees under 37 C.F.R. § 1.16 for the presentation of extra claims
- ☒ Any patent application processing fees under 37 C.F.R. § 1.17

Respectfully submitted,  
HOGAN & HARTSON L.L.P.

By: Erin P. Madill  
Erin P. Madill  
Registration No. 46,893  
Attorneys for Applicant

Date: March 14, 2003

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## IMPORTANT NOTICE TELECOPY/FACSIMILE COVER LETTER

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\*Affiliated Office

TO: U.S. Patent and Trademark Office  
Examiner: Phat X. Cao  
Art Unit: 2814

DATE: March 14, 2003

FROM: Erin P. Madill

TIME: \_\_\_\_\_

TOTAL NO. OF PAGES, INCLUDING COVER: 8

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### MESSAGE:

Patent Application No.: 09/754,818; Our Ref. 81754.0048

I hereby certify that the following documents:

- ☒ Amendment Transmittal
- ☒ Amendment Under 37 C.F.R. § 1.116

are being facsimiled to the Commissioner for Patents, Washington, D.C. 20231.

March 14, 2003  
Date of Deposit

Diane Zynn  
Diane Zynn

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TECHNOLOGY CENTER 2800

TELECOPY/FAX NUMBER: 703-872-9319 ART UNIT 2814  
CLIENT NUMBER: 81754.0048  
ATTORNEY BILLING NUMBER: 3606  
CONFIRMATION NUMBER: 703-308-4917 (return fax to Diane Zynn)

PATENT 81754.0048

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Toshihiko HIGUCHI

Serial No: 09/754,818

Filed: January 4, 2001

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WIRING PATTERN AND METHOD FOR  
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03/14/03  
Date

AMENDMENT UNDER 37 C.F.R. § 1.116

Box AF  
Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

In response to the Final Office Action dated December 17, 2002, please amend the  
above-referenced application as follows:

IN THE CLAIMS:

Please replace the text of claim 1 with the following text:

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MAR 14 2003

TECHNOLOGY CENTER 2800

1. (Amended) A semiconductor device having a wiring pattern that is  
formed by etching a conductive layer using a resist pattern as a mask, the semiconductor  
device comprising:  
a contact section formed in an interlayer dielectric layer;  
a first wiring formed over the interlayer dielectric layer and disposed with a separation  
from the contact section shorter than a specified separation; and  
a second wiring having a connection region connected to the contact section,  
wherein the second wiring has an extension section extending in a non-wiring region in  
the connection region connected to the contact section, and  
the extension section extends from at least one section of the connection region other  
than sides of the connections region facing the first wiring.